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3	BRS	L3	11	L1 and L2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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2	2006/02/17 18:17
3	2006/02/17 18:18

	Type	L #	Hits	Search Text	DBs
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	Time Stamp
4	2006/02/17 18:18
5	2006/02/17 18:18
6	2006/02/17 18:18

	Type	L #	Hits	Search Text	DBs
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9	BRS	L9	273	713/181.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp
7	2006/02/17 18:18
8	2006/02/17 18:18
9	2006/02/17 18:18

	Type	L #	Hits	Search Text	DBs
10	BRS	L10	1076	380/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	681	380/44.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	975	380/277.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp
10	2006/02/17 18:19
11	2006/02/17 18:19
12	2006/02/17 18:19

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	375	380/278.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	217	380/286.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	8927	(software module) near (embedded key)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp
13	2006/02/17 18:19
14	2006/02/17 18:19
15	2006/02/17 18:20

	Type	L #	Hits	Search Text	DBs
16	BRS	L16	5026	(software module) near (embedded key) and (trusted source)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L17	1541	(trusted source) near (certificate chain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L18	96	L16 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp
16	2006/02/17 18:20
17	2006/02/17 18:20
18	2006/02/17 18:21

	Type	L #	Hits	Search Text	DBs
19	BRS	L19	580269	(read$3) and (software module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L20	92	L18 and L19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L21	27	L20 and "traceable"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp
19	2006/02/17 18:21
20	2006/02/17 18:21
21	2006/02/17 18:21

	Type	L #	Hits	Search Text	DBs
22	BRS	L22	3	L21 and "certificate authority"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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22	2006/02/17 18:22

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1 Link and channel measurement: A simple mechanism for capturing and replaying wireless channels

Glenn Judd, Peter Steenkiste
August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05**
Publisher: ACM Press
Full text available: pdf(6.06 MB) Additional Information: full citation, abstract, references, index terms

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility of using on-card signal strength measurements to capture wireless channel traces. A key advantage of this approach is the simplicity and ubiquity with which these measurements can be obtained since virtually all wireless devices provide the req ...

Keywords: channel capture, emulation, wireless

2 Frontmatter (TOC, Letters, Election results, Software Reliability Resources!, Computing Curricula 2004 and the Software Engineering Volume SE2004, Software Reuse Research, ICSE 2005 Forward)

July 2005 **ACM SIGSOFT Software Engineering Notes**, Volume 30 Issue 4
Publisher: ACM Press
Full text available: pdf(6.19 MB) Additional Information: full citation, index terms

3 Security as a new dimension in embedded system design: Security as a new dimension in embedded system design

Srivaths Ravi, Paul Kocher, Ruby Lee, Gary McGraw, Anand Raghunathan
June 2004 **Proceedings of the 41st annual conference on Design automation**
Publisher: ACM Press
Full text available: pdf(209.10 KB) Additional Information: full citation, abstract, references, citings, index terms

The growing number of instances of breaches in information security in the last few years has created a compelling case for efforts towards secure electronic systems. Embedded

systems, which will be ubiquitously used to capture, store, manipulate, and access data of a sensitive nature, pose several unique and interesting security challenges. Security has been the subject of intensive research in the areas of cryptography, computing, and networking. However, despite these efforts, *security is ...*

***Keywords**: PDAs, architectures, battery life, cryptography, design, design methodologies, digital rights management, embedded systems, performance, security, security processing, security protocols, sensors, software attacks, tamper resistance, trusted computing, viruses*

4 Security in embedded systems: Design challenges

Srivaths Ravi, Anand Raghunathan, Paul Kocher, Sunil Hattangady

August 2004 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 3 Issue 3

Publisher: ACM Press

Full text available: pdf(3.67 MB) Additional Information: full citation, abstract, references, index terms, review

Many modern electronic systems---including personal computers, PDAs, cell phones, network routers, smart cards, and networked sensors to name a few---need to access, store, manipulate, or communicate sensitive information, making security a serious concern in their design. Embedded systems, which account for a wide range of products from the electronics, semiconductor, telecommunications, and networking industries, face some of the most demanding security concerns---on the one hand, they are oft ...

Keywords: Embedded systems, architecture, authentication, battery life, cryptographic algorithms, decryption, encryption, hardware design, processing requirements, security, security attacks, security protocols, tamper resistance

5 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Publisher: IBM Press

Full text available: pdf(4.21 MB) Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

6 Computing curricula 2001

September 2001 **Journal on Educational Resources in Computing (JERIC)**

Publisher: ACM Press

Full text available: pdf(613.63 KB) html(2.78 KB) Additional Information: full citation, references, citings, index terms

7

Frontmatter (TOC, Letters, Philosophy of computer science, Interviewers needed, Taking software requirements creation from folklore to analysis, SW components and product lines: from business to systems and technology, Software engineering

survey)
September 2005 **ACM SIGSOFT Software Engineering Notes**, Volume 30 Issue 5
Publisher: ACM Press
Full text available: pdf(1.98 MB) Additional Information: full citation

8 Session summaries from the 17th symposium on operating systems principle (SOSP'99)
Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2
Publisher: ACM Press
Full text available: pdf(3.15 MB) Additional Information: full citation, index terms

9 Seeing, hearing, and touching: putting it all together
Brian Fisher, Sidney Fels, Karon MacLean, Tamara Munzner, Ronald Rensink
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**
Publisher: ACM Press
Full text available: pdf(20.64 MB) Additional Information: full citation

10 Workshop on architectural support for security and anti-virus (WASSA): Towards the issues in architectural support for protection of software execution
Weidong Shi, Hsien-Hsin S. Lee, Chenghuai Lu, Mrinmoy Ghosh
March 2005 **ACM SIGARCH Computer Architecture News**, Volume 33 Issue 1
Publisher: ACM Press
Full text available: pdf(436.30 KB) Additional Information: full citation, abstract, references, index terms

Recently, there is a growing interest in the research community to employ tamper-resistant processors for software protection. Many of these proposed systems rely on a specially tailored secure processor to prevent 1) illegal software duplication, 2) unauthorized software modification, and 3) unauthorized software reverse engineering. Most of these works primarily focus on the feasibility demonstration and design details rather than trying to elucidate many fundamental issues that are either "el ...

Keywords: attack, copy protection, encryption, security, tamper resistance

11 A structural view of the Cedar programming environment
Daniel C. Swinehart, Polle T. Zellweger, Richard J. Beach, Robert B. Hagmann
August 1986 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 8 Issue 4
Publisher: ACM Press
Full text available: pdf(6.32 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper presents an overview of the Cedar programming environment, focusing on its overall structure—that is, the major components of Cedar and the way they are organized. Cedar supports the development of programs written in a single programming language, also called Cedar. Its primary purpose is to increase the productivity of programmers whose activities include experimental programming and the development of prototype software systems for a high-performance personal computer. T ...

12 Operating System Structures to Support Security and Reliable Software
Theodore A. Linden
December 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 4
Publisher: ACM Press
Full text available: pdf(3.49 MB) Additional Information: full citation, references, citings, index terms

13 Innovative systemic perspectives: Effective work practices for software engineering: free/libre open source software development
Kevin Crowston, Hala Annabi, James Howison, Chengetai Masango
November 2004 **Proceedings of the 2004 ACM workshop on Interdisciplinary software engineering research**
Publisher: ACM Press
Full text available: pdf(390.40 KB) Additional Information: full citation, abstract, references, index terms

We review the literature on Free/Libre Open Source Software (FLOSS) development and on software development, distributed work and teams more generally to develop a theoretical model to explain the performance of FLOSS teams. The proposed model is based on Hackman's [34] model of effectiveness of work teams, with coordination theory [52] and collective mind [79] to extend Hackman's model by elaborating team practices relevant to effectiveness in software development. We propose a set of propos ...

Keywords: collective mind theory, coordination theory, free and open source software, team effectiveness

14 Trustworthy 100-year digital objects: Evidence after every witness is dead
Henry M. Gladney
July 2004 **ACM Transactions on Information Systems (TOIS)**, Volume 22 Issue 3
Publisher: ACM Press
Full text available: pdf(1.24 MB) Additional Information: full citation, abstract, references, index terms

In ancient times, wax seals impressed with signet rings were affixed to documents as evidence of their authenticity. A digital counterpart is a message authentication code fixed firmly to each important document. If a digital object is sealed together with its own audit trail, each user can examine this evidence to decide whether to trust the content---no matter how distant this user is in time, space, and social affiliation from the document's source.We propose an architecture and design that a ...

15 Software economics: a roadmap
Barry W. Boehm, Kevin J. Sullivan
May 2000 **Proceedings of the Conference on The Future of Software Engineering**
Publisher: ACM Press
Full text available: pdf(2.58 MB) Additional Information: full citation, references, citings, index terms

16 Version models for software configuration management
Reidar Conradi, Bernhard Westfechtel
June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2
Publisher: ACM Press
Full text available: pdf(483.54 KB) Additional Information: full citation, abstract, references, citings, index terms

After more than 20 years of research and practice in software configuration management

(SCM), constructing consistent configurations of versioned software products still remains a challenge. This article focuses on the version models underlying both commercial systems and research prototypes. It provides an overview and classification of different versioning paradigms and defines and relates fundamental concepts such as revisions, variants, configurations, and changes. In particular, we foc ...

Keywords: changes, configuration rules, configurations, revisions, variants, versions

17 Invited talk: Source transformation, analysis and generation in TXL

James R. Cordy
January 2006 **Proceedings of the 2006 ACM SIGPLAN symposium on Partial evaluation and semantics-based program manipulation PEPM '06**
Publisher: ACM Press
Full text available: pdf(914.77 KB) Additional Information: full citation, abstract, references, index terms

The TXL transformation framework has been widely used in practical source transformation tasks in industry and academia for many years. At the core of the framework is the TXL language, a functional programming language specifically designed for expressing source transformation tasks. TXL programs are self-contained, specifying and implementing all aspects of parsing, pattern matching, transformation rules, application strategies and unparsing in a single uniform notation with no dependence on o ...

Keywords: TXL, migration, rapid prototyping, re-engineering, rule-based programming, software analysis, source transformation, term rewriting, test-driven development, translators

18 Guidance for the use of the Ada programming language in high integrity systems

B. A. Wichmann
July 1998 **ACM SIGAda Ada Letters**, Volume XVIII Issue 4
Publisher: ACM Press
Full text available: pdf(2.93 MB) Additional Information: full citation, abstract, citings, index terms

This paper is the current result of a study by the ISO HRG Rapporteur group which is being circulated for comment. Many people have contributed to this, but those who have either attended two recent meetings of group or have made substantial e-mail comments are: Praful V Bhansali (Boeing, USA), Alan Burns (University of York, UK), Bernard Carre' (Praxis Critical Systems, UK), Dan Craigen (ORA, Canada), Nick Johnson MoD, UK), Stephen Michell (Canada), Gilles Motet (DGEI/INSA, France), George Roma ...

19 (How) can mobile agents do secure electronic transactions on untrusted hosts? A survey of the security issues and the current solutions

Joris Claessens, Bart Preneel, Joos Vandewalle
February 2003 **ACM Transactions on Internet Technology (TOIT)**, Volume 3 Issue 1
Publisher: ACM Press
Full text available: pdf(197.96 KB) Additional Information: full citation, abstract, references, citings, index terms

This article investigates if and how mobile agents can execute secure electronic transactions on untrusted hosts. An overview of the security issues of mobile agents is first given. The problem of untrusted (i.e., potentially malicious) hosts is one of these issues, and appears to be the most difficult to solve. The current approaches to counter this problem are evaluated, and their relevance for secure electronic transactions is discussed. In particular, a state-of-the-art survey of mobile agen ...

Keywords: Mobile agent security, electronic transactions, malicious hosts

20 Formalizing the safety of Java, the Java virtual machine, and Java card





Pieter H. Hartel, Luc Moreau
December 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 4
Publisher: ACM Press

Full text available: pdf(442.86 KB) Additional Information: full citation, abstract, references, citings, index terms

We review the existing literature on Java safety, emphasizing formal approaches, and the impact of Java safety on small footprint devices such as smartcards. The conclusion is that although a lot of good work has been done, a more concerted effort is needed to build a coherent set of machine-readable formal models of the whole of Java and its implementation. This is a formidable task but we believe it is essential to build trust in Java safety, and thence to achieve ITSEC level 6 or Common Crite ...

Keywords: Common criteria, programming

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United States Patent Application: 0050132194
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